# Georgia Institute of Technology

## School of Electrical and Computer Engineering

ECE 3043	Electrical and Electronic Circuits Laboratory			Verification Sheet	
NAME:			SECTION:		
AD LOGIN:					
	Experime	ent 13: Common Sour	ce Amplifier		
Procedure	Time Completed	Date Completed	Verification (Must demonstrate circuit)	Points Possible	Points Received
2. Parameter Measurement				25	
3. Bias				25	
3. Small Signal Gain and Frequency Response				25	
3. Clipping				25	
procedures during you	ur scheduled lab perio	od or spend your entir	hours, you must comp e scheduled lab session	n attemptin	g to do so.
	iment and receive ful	ll credit on the report.	nson permits you to att Without this signature	-	
SIGNATURE:		DATE	<u>:</u>		

### **ECE 3043 Check-off Requirements for Experiment 13**

Make sure you have made all required measurements before requesting a check-off. For all check-offs, you must demonstrate the circuit or measurement to a lab instructor. All screen captures must have a time/date stamp.

#### 2. Parameter Measurement

- ✓ Table of I<sub>D</sub> versus V<sub>GS</sub> for constant V<sub>DS</sub>
- ✓ Plot of square root of I<sub>D</sub> versus V<sub>GS</sub>
- ✓ Calculation of K and V<sub>T</sub>

#### 3. Bias

✓ Drain, gate, source voltages and drain current recorded.

#### 3. Gain and Frequency Response

- ✓ Oscilloscope screen capture showing input and output signals and Vpp measurements for each signal.
- ✓ Calculation of the gain.
- ✓ Plot of gain versus frequency made with HPVEE, LabView, or by hand with -3dB frequencies and midband gain labeled and their values recorded.

#### 3. Clipping

- ✓ Screen capture showing soft clipping on output and measured positive and negative peak amplitudes (use max and min functions on scope).
- ✓ Screen capture showing hard clipping on output and measured positive and negative clipping levels (use max and min functions on scope).